

| Name of the faculty | Dr. SUDHIR K. BHIL |
|-----------------------------------|---|
| Designation | LECTURER(SELECTION GRADE) |
| Qualification | PhD In Electrical Engineering |
| | M tech In Electrical(Specialization in |
| | Control System) |
| | B.E. (Electrical) |
| Date of Joining | January 1997 |
| Contact no./E-mail | 8898723248/skbhil@ee.vjti.ac.in |
| Area of Research | Power System, Power electonics |
| Subject Taught | Switchgear Protection, Smart Grid, |
| | Power System |
| Publications | Presented paper in 6th international |
| | conference on Control, Decision and |
| | Information Technology (CODIT-2019) |
| | Paris,France |
| | Presented paper in Australian Control |
| | conference (AUCC) 2013 Fremantle, |
| | Western Australia, (3 to 5th November |
| | 2013) |
| | |
| Guidance | Project guide for final year diploma in |
| | electrical engineering students. |
| Collaboration/Interaction on with | |
| outside word | |
| Membership of professional body | Indian society for technical education- |
| | ISTE-Life Member |